Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.

**ANODE**

**.024 x .024”**

**.034”**

**.034”**

**Top Material: Al**

**Backside Material: Au**

**Bond Pad Size: .024 x .024”**

**Backside Potential: CATHODE**

**Mask Ref:**

**APPROVED BY: DK DIE SIZE .034” X .034” DATE: 9/2/21**

**MFG: UNITRODE THICKNESS .012” P/N: 1N5804**

**DG 10.1.2**

#### Rev B, 7/1